

# LCD MODULE SPECIFICATION

Model: CG12864C - \_ \_ - \_ - \_ - \_

Revision	03
Engineering	Timothy Chan
Date	4 November 2019
Our Reference	X9054

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#### **MODE OF DISPLAY**

Display mode **Display condition** Viewing direction STN: Yellow green Reflective type 6 O' clock Grey Transflective type 12 O' clock Blue (negative) Transmissive type 3 O' clock Others FSTN positive 9 O' clock FSTN negative

#### LCD MODULE NUMBER NOTATION:

- \*(1)---Model number of standard LCD Modules
- \*(2)---Backlight type

N – No backlight

E – EL backlight

L – Side-lited LED backlight

M- Array LED backlight

C-CCFL

\*(3)---Backlight color

N – No backlight

A - Amber

B - Blue

O- Orange

W-White

Y – Yellow green

G-Green

R-Red

\*(4)---Display mode

T - TN

V – TN (Negative)

S - STN Yellow green

G – STN Grey

B – STN Blue (Negative)

F - FSTN

N – FSTN (Negative)

\*(5)---Rear polarizer type

R – Reflective

F – Transflective

r = Transflective

T-Transmissive

\*(6)---Temperature range

N-Normal

W- Extended

\*(7)---Viewing direction

6 - 6 O'clock

2 – 12 O'clock

3 – 3 O'clock

9 – 9 O'clock

\*(8)---Special code for other requirements (Can be omitted if not used)

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### **GENERAL DESCRIPTION**

Display mode : 128 x 64 dots, Graphic COG LCD module

Interface : Serial

Driving method : 1/65 duty, 1/9 bias

Controller IC : Sitronix ST7565R or equivalent

For the detailed information, please refer to the IC specifications

### **MECHANICAL DIMENSIONS**

Item	Dimension	Unit	Item	Dimension	Unit
No Backlight (N)	34.4(L)x32.6(W)x2.1max(H)	mm	Viewing Area	30.4(L)x20.9(W)	mm
LED Sided Backlight(L)	34.4(L)x32.6(W)x5.7max(H)	mm	Dot Pitch	0.22(L)x0.30(W)	mm
RGB Backlight	34.4(L)x32.6(W)x6.4max(H)	mm	Dot Size	0.195(L)x0.275(W)	mm

# **CONNECTOR PIN ASSIGNMENT**

### CN1

Pin No.	Symbol	Function
1	V0	
2	V1	
3	V2	Power supply for LCD
4	V3	
5	V4	
6	CAP2N	
7	CAP2P	
8	CAP1P	Voltage Converter
9	CAP1N	voltage Converter
10	CAP3P	
11	VOUT	Voltage Converter Input / Output
12	VSS	Ground
13	VDD	Power Supply for Logic
14	D7(SI)	Serial Data Input pin
15	D6(SCL)	Serial Clock Input pin
16	A0	Register Select pin
17	RST	External Reset input
18	CS1B	Chip Select

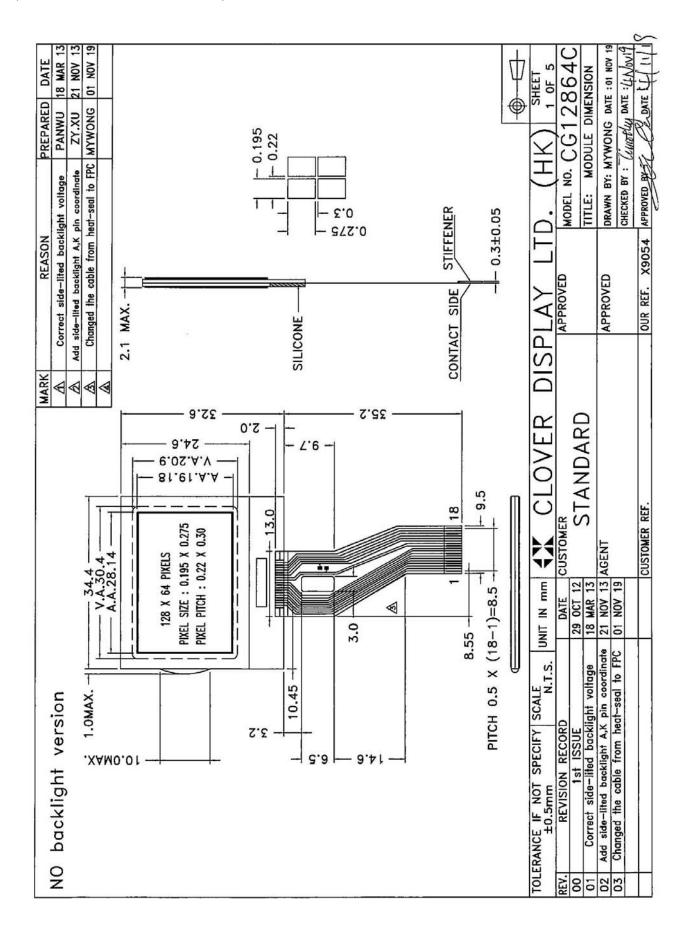
# CN2

<u> </u>		
Pin No.	Symbol	Function
(*)19	A	Supply voltage for backlight (+)
(*)20	K	Supply voltage for backlight (-)
(*)19	A	Supply voltage for backlight (+)
(*)20	KR	Supply voltage for backlight (-)
(*)21	KG	Supply voltage for backlight (-)
(*)22	KB	Supply voltage for backlight (-)

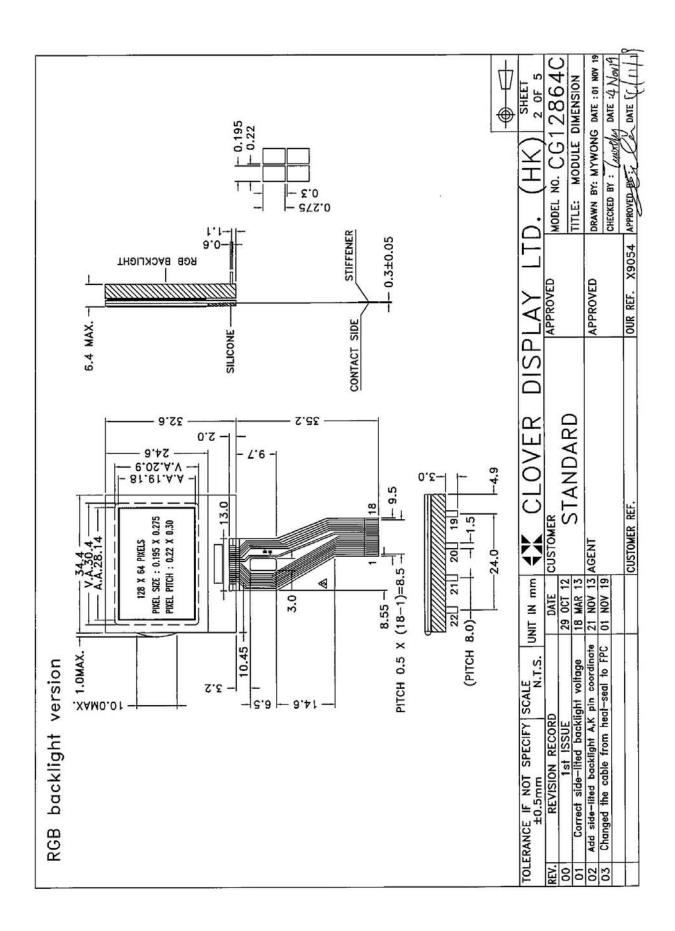
Note (\*): CN2 are for backlight versions only.

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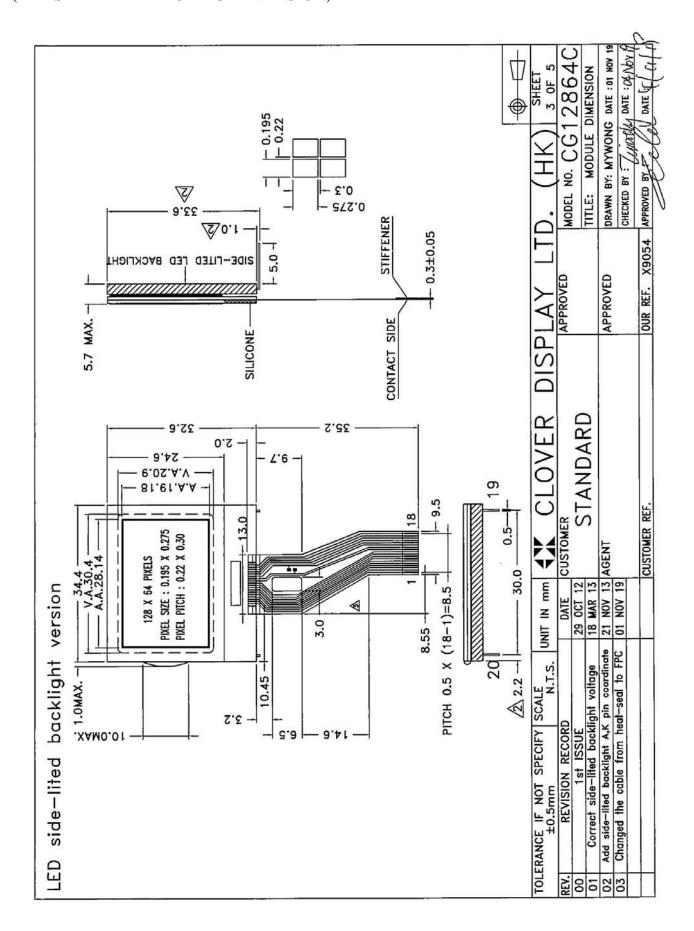
# COUNTER DRAWING OF MODULE DIMENSION (NO BACKLIGHT VERSION)



# COUNTER DRAWING OF MODULE DIMENSION (RGB BACKLIGHT VERSION)



# COUNTER DRAWING OF MODULE DIMENSION (LED SIDE-LITED BACKLIGHT VERSION)



# COUNTER DRAWING OF PIN OUT & BLOCK DIAGRAM

	FUNCTION			Power supply for LCD	24 VALUE				Voltage converter			Voltage converter input/output	Ground	Power supply for logic	Serial data input pin	Serial clock input pin	Register select input	External reset input	Chip select	- Canada - C	FUNCTION	Supply voltage for backlight (+)	Supply voltage for backlight (-)	Supply voltage for backlight (+)	voltage	voltage for backlight	Supply voltage for backlight (-)	LTD. (HK) A OF 5	ED MODEL NO. CG12864C	TITLE: PIN OUT & BLOCK DIAGRAM		CHECKED BY : JUNE PLY DATE : DG NOW!	OUR REF. X9054 APPROVED BY: 4 CL DATE ( [1/1]
	SYMBOL	0.0	۷1	٧2	٧3	۷4	CAP2N	CAP2P	CAP1P	CAP1N	CAP3P	VOUT	VSS	VDD	D7(SI)	De(SCL)	AO	RST	CS1B		SYMBOL	Ą	×	A	X.	S !	ΚB	۲A۲	APPROVED		APPROVED		OUR REF
CN1	PIN NO.	-	2	3	4	വ	9	, ,	8	6	10	=	12	13	14	15	16	17	18	CN2	PIN NO.	(*)19	(*)20	(*)	(*)20	(*)21	(*)22	DISF					
						128 SEG				ST7565R	OR	EQUIVALENCE				ed backlight			RGB backlight			Side Lifed LFD		200	s Olliy RGB			CLOVER	CUSTOME	SIANDARD	AGENT		CUSTOMER REF.
			64	, co	<u>M</u>			7111				E				LED side-lifed			RGB bo					vlac sacistav tabildo				N.T.S. UNIT IN mm	DATE	age 18 MAR 13		10 rrc 01 NOV 19	
		ţ	1	CAPZN	CAP2P	CAPTP	CAPIN	Vout	NSS .	VDD DZ(SI)	D(S)	A0	RST	CS1B		4 3		\ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \ \	KK	2 5				(*). CNO are for backlight	Noie (*). CNZ die 101 Da			TOLERANCE IF NOT SPECIFY SCALE ±0.5mm N.	REVISION RECORD	Correct side-lited backlight voltage	Add side-lited backlight A,K pin coordinate	Changed the cable from near-seal to Fro	

Conditions: VSS=0V, @Ta=25°C

### **ELECTRICAL CHARACTERISTICS**

Item	Symbol	MIN.	TYP.	MAX.	Unit
Supply Voltage for Logic	VDD	2.75	3.0	3.3	V
Supply Current for Logic	IDD	_	0.18	0.26	mA
Operating Voltage for LCD (*)	V0	10.45	11.0	11.55	V
'High' Level Input Voltage	VIH	0.8VDD	_	_	V
'Low' Level Input Voltage	VIL	_	_	0.2VDD	V

Note (\*): There is tolerance in optimum LCD driving voltage during production and it will be within the specified range.

#### Side BL:

### Constant voltage driving:

Item	Symbol	MIN.	TYP.	MAX.	Unit	Condition
Red	$I_{\mathrm{BL}}$	_	20	30	mA	$V_{BL} = 3.5V$
White	$I_{\mathrm{BL}}$	_	12	14	mA	$V_{BL} = 3.5V$
Green	$I_{\mathrm{BL}}$	_	20	30	mA	$V_{BL} = 3.5V$
Blue	$I_{\mathrm{BL}}$	_	20	30	mA	$V_{BL} = 3.5V$

#### RGB BL:

#### Constant voltage driving:

Item	Symbol	MIN.	TYP.	MAX.	Unit	Condition
Red	$I_{BL}$	_	14	20	mA	$V_{\rm BL} = 5.0 V$
Green	$I_{BL}$	_	22	30	mA	$V_{\rm BL} = 5.0 V$
Blue	$I_{BL}$	_	10	16	mA	$V_{\rm BL} = 5.0 V$

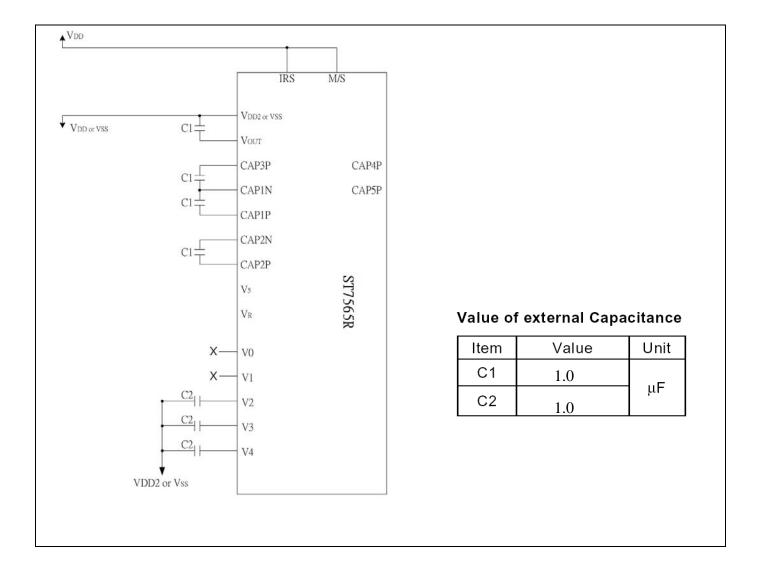
### **ABSOLUTE MAXIMUM RATINGS**

Please make sure not to exceed the following maximum rating values under the worst application conditions

Item	Symbol	Rating (for normal temperature)	Rating (for wide temperature)	Unit
Supply Voltage	Vdd	-0.3 to 3.6	-0.3 to 3.6	V
Input Voltage	VT	-0.3 to 13.5	-0.3 to 13.5	V
Operating Temperature	Topr	0 to 50	-20 to 70	$^{\circ}\!\mathbb{C}$
Storage Temperature	Tstg	-10 to 60	-30 to 80	$^{\circ}\!\mathbb{C}$

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# REFERENCE CIRCUIT EXAMPLE



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### **INSTRUCTION TABLE**

		R/W			С	DESCRIPTION						
INSTRUCTION	Α0	(RWR)	D7	D6	D5	D4	D3	D2	D1	D0	DESCRIPTION	
Display ON/OFF	0	0	1	0	1	0	1	1	1	D	D=1, display ON D=0, display OFF	
Set Start Line	0	0	0	1	S5	S4	S3	S2	S1	S0	Set display start line	
Set Page Address	0	0	1	0	1	1	Y3	Y2	Y1	Y0	Set page address	
Set Column Address	0	0	0	0	0	1	X7	X6	X5	X4	Set column address (MSB)	
Oct Column Address	0	0	0	0	0	0	Х3	X2	X1	X0	Set column address (LSB)	
Read Status	0	1	BUSY	MX	D	RST	0	0	0	0	Read IC Status	
Write Data	1	0	D7	D6	D5	D4	D3	D2	D1	D0	Write display data to RAM	
Read Data	1	1	D7	D6	D5	D4	D3	D2	D1	D0	Read display data from RAM	
SEG Direction	0	0	1	0	1	0	0	0	0	MX	Set scan direction of SEG MX=1, reverse direction MX=0, normal direction	
Inverse Display	0	0	1	0	1	0	0	1	1	INV	INV =1, inverse display INV =0, normal display	
All Pixel ON	0	0	1	0	1	0	0	1	0	AP	AP=1, set all pixel ON AP=0, normal display	
Bias Select	0	0	1	0	1	0	0	0	1	BS	Select bias setting 0=1/9; 1=1/7 (at 1/65 duty)	
Read-modify-Write	0	0	1	1	1	0	0	0	0	0	Column address increment: Read:+0 , Write:+1	
END	0	0	1	1	1	0	1	1	1	0	Exit Read-modify-Write mode	
RESET	0	0	1	1	1	0	0	0	1	0	Software reset	
COM Direction	0	0	1	1	0	0	MY	-	-	-	Set output direction of COM MY=1, reverse direction MY=0, normal direction	
Power Control	0	0	0	0	1	0	1	VB	VR	VF	Control built-in power circuit ON/OFF	
Regulation Ratio	0	0	0	0	1	0	0	RR2	RR1	RR0	Select regulation resistor ratio	
Set EV	0	0	1	0	0	0	0	0	0	1	Double command!! Set	
Set EV	0	0	0	0	EV5	EV4	EV3	EV2	EV1	EV0	electronic volume (EV) level	
Power Save Mode Set	0	0	1	0	1	0	1	1	0	MD	MD=0, sleep mode	
rower save mode set	0	0	0	0	0	0	0	0	0	0	MD=1, normal	
Power Save	0	0			Cor	mpound	Comm	and			Display OFF + All Pixel ON	
0.15	0	0	1	1	1	1	1	0	0	0	Double command!! Set booster level:	
Set Booster	0	0	0	0	0	0	0	0	BL1	BL0	BL[1:0]=(0,0), x2, x3, x4 BL[1:0]=(0,1), x5 BL[1:0]=(1,1), x6	
NOP	0	0	1	1	1	0	0	0	1	1	No operation	
Test	0	0	1	1	1	1	-	-	-	-	Do NOT use. Reserved for testing.	

Note: Symbol "-" means this bit can be "H" or "L".

# RECOMMENDED INITIAL SETTINGS

Set Start Line: 40H Set Page Address: B0H Set COM Direction: C8H Set SEG Direction: A0H Set LCD Bias Select: A2H Set Power Control: 2FH

Set Regulation Ratio: 27H Set Electronic volume register: 81H 1FH

Set Display On: AFH

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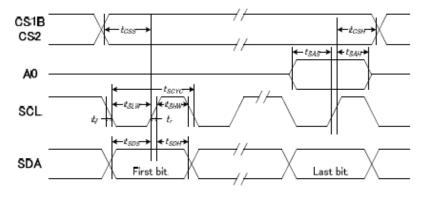
# DISPLAY DATA RAM (DDRAM)

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					118	130	ē	123	138	8	241	243	245	246	248	PAD N											

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#### SERIAL INTERFACE TIMING DIAGRAM

### System Bus Timing for 4-Line Serial Interface



(VDD = 3.3V, Ta =25°C)

Item	Signal	Symbol	Condition	Min.	Max.	Unit
Serial clock period		tSCYC		50	_	
SCLK "H" pulse width	SCLK	tSHW		25	_	
SCLK "L" pulse width		tSLW		25	_	
Address setup time	40	tSAS		20	_	
Address hold time	A0	tSAH		10	_	ns
Data setup time	eD4	tSDS		20	_	
Data hold time	SDA	tSDH		10	_	
CS-SCLK time	CS1B	tCSS		20	_	
CS-SCLK time	CS2	tCSH		40	_	

(VDD = 2.7V, Ta =25°C)

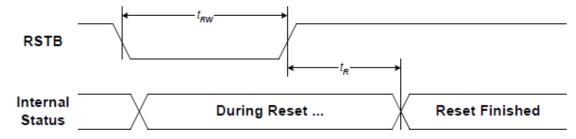
Item	Signal	Symbol	Condition	Min.	Max.	Unit
Serial clock period		tSCYC		100	_	
SCLK "H" pulse width	SCLK	tSHW		50	_	
SCLK "L" pulse width		tSLW		50	_	
Address setup time	A0	tSAS		30	_	
Address hold time		tSAH		20	_	ns
Data setup time	0DA	tSDS		30	_	
Data hold time	SDA	tSDH		20	_	
CS-SCLK time	CS1B	tCSS		30	_	
CS-SCLK time	CS2	tCSH		60	_	

<sup>\*1</sup> The input signal rise and fall time (tr, tf) are specified at 15 ns or less.

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<sup>\*2</sup> All timing is specified using 20% and 80% of VDD as the standard.

#### **RESET TIMING**



(VDD = 3.3V, Ta =25°C)

Item	Symbol	Condition	Min.	Max.	Unit
Reset time	tR		_	1.0	
Reset "L" pulse width	tRW		1.0	_	us

(VDD = 2.7V, Ta =25°C)

Item	Symbol	Condition	Min.	Max.	Unit
Reset time	tR		_	2.0	
Reset "L" pulse width	tRW		2.0	_	us

#### THE RESET CIRCUIT

Setting RSTB to "L" can initialize internal function. While RSTB is "L", no instruction except read status can be accepted. RSTB pin must connect to the reset pin of MPU and initialization by RSTB pin is essential before operating. Please note the hardware reset is not same as the software reset. When RSTB becomes "L", the hardware reset procedure will start. When RESET instruction is executed, the software reset procedure will start. The procedure is listed below:

Procedure	Hardware Reset	Software Reset
Display OFF: D=0, all SEGs/COMs output at VSS	V	Х
Normal Display: INV=0, AP=0	V	Х
SEG Normal Direction: MX=0	V	X
Clear Serial Counter and Shift Register (if using Serial Interface)	V	Х
Bias Selection: BS=0	V	X
Booster Level BL=0	V	Х
Exit Power Saving Mode	V	X
Power Control OFF: VB=0, VR=0, VF=0	V	Х
Exit Read-modify-Write mode	V	V
Static Indicator OFF	V	V
Static Indicator Register SIR[1:0]=(0,0)	V	V
Start Line S[5:0]=0	V	V
Column Address X[7:0]=0	V	V
Page Address Y[3:0]=0	V	V
COM Normal Direction: MY=0	V	V
V0 Regulation Ratio RR[2:0]=(1,0,0)	V	V
EV[5:0]=(1,0,0,0,0,0)	V	V
Exit Test Mode	V	V

After power-on, RAM data are undefined and the display status is "Display OFF". It's better to initialize whole DDRAM (ex: fill all 00h or write the display pattern) before turning the Display ON. Besides, the power is not stable at the time that the power is just turned ON. A hardware reset is needed to initialize those internal registers after the power is stable.

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# **ELECTRO-OPTICAL CHARACTERISTICS**

MEASURING CONDITION: POWER SUPPLY =  $V_{OP}$  / 64 Hz

TEMPERATURE =  $23 \pm 5$  °C RELATIVE HUMIDITY =  $60 \pm 20$  %

ITEM	SYMBOL	UNIT	TYP. STN
RESPONSE TIME	Ton	ms	220
	Toff	ms	280
CONTRAST RATIO	Cr	-	12
	V3:00	0	40
VIEWING ANGLE	V6:00	0	70
(6 O'clock)	V9:00	0	40
$Cr \ge 2$	V12:00	0	50

THE ELECTRO-OPTICAL CHARACTERISTICS ARE MEASURED VALUE BUT NOT GUARANTEED ONES.

# RELIABILITY OF LCD MODULE

	TEST CONDITION	TEST CONDITION	
ITEM	FOR NORMAL TEMPERATURE	FOR WIDE TEMPERATURE	TIME
High temperature operating	50°C	70°C	240 hours
Low temperature operating	0°C	-20°C	240 hours
High temperature storage	60°C	80°C	240 hours
Low temperature storage	-10°C	-30°C	240 hours
Temperature-humidity storage	40°C 90% R.H.	60°C 90% R.H.	96 hours
Temperature cycling	-10°C to 60°C	-30°C to 80°C	5 ovele
	30 Min Dwell	30 Min Dwell	5 cycle
Vibration Test at LCM Level	Freq 10-55 Hz	Freq 10-55 Hz	
	Sweep rate: 10-55-10 at 1 min	Sweep rate: 10-55-10 at 1 min	
	Sweep mode Linear	Sweep mode Linear	_
	Displacement: 2 mm p-p	Displacement: 2 mm p-p	
	1 Hour each for X, Y, Z	1 Hour each for X, Y, Z	

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### **SAMPLING METHOD**

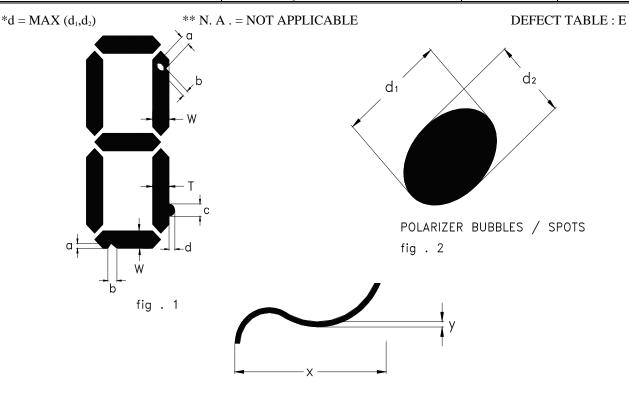
SAMPLING PLAN: ANSI/ASQ Z1.4

CLASS OF AQL: LEVEL II/ SINGLE SAMPLING

MAJOR-0.65% MINOR – 1.5%

# **QUALITY STANDARD**

DEFECT	CRITERI	A	ТҮРЕ	FIGURE
SHORT CIRCUIT	-		MAJOR	-
MISSING SEGMENT	-		MAJOR	-
UNEVEN / POOR CONTRAST	-		MAJOR	-
CROSS TALK	-		MAJOR	-
PIN HOLE	$MAX(a,b) \leq$	1/3 W	MINOR	1
EXCESS SEGMENT	$MAX(c,d) \leq$	1 / 3 T	MINOR	1
BUBBLES	d* ≥ 0.5	QTY=0	MINOR	2
SPOTS	d ≤ 0.6	N.A.**	MINOR	2
	0.6 <d≤0.7< td=""><td>QTY≤2</td><td></td><td></td></d≤0.7<>	QTY≤2		
	0.7 <d< td=""><td>QTY=0</td><td></td><td></td></d<>	QTY=0		
LINE SCRATCHES	x≥0.7 y≥0.05	QTY=0	MINOR	3
BLACK LINE	x≥0.7 y≥0.05	QTY=0	MINOR	3



LINE SCRATCHES / BLACK LINE fig . 3

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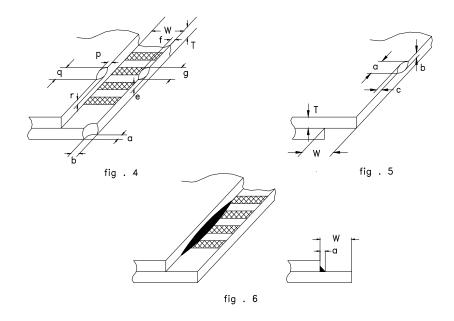
# ${\bf QUALITY\ STANDARD\ (\ CONT\ .)}$

	DEFECT	CRITERIA	ТҮРЕ	FIGURE
	CONTACT EDGE	e≤T f≤1/2W g:N.A.		4
CHIPS	BOTTOM GLASS	p≤V.A.*** q:N.A. r≤T	MINOR	4
	CORNER	a:N.A. b≤W		4
	TOP GLASS	a:N.A. b≤T c≤W		5
GLASS P	GLASS PROTRUSION $a \le 1/3 \text{ W}$		MINOR	6
RAINBOW		-	MINOR	-

UNLESS STATE OTHERWISE , ALL UNIT ARE IN MILLIMETER .

\*\*\*CANNOT EXTEND IN V.A.

DEFECT TABLE : E



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#### HANDLING PRECAUTIONS

#### (1) CAUTION OF LCD HANDLING & CLEANING

Use soft cloth with solvent (recommended below) to clean the display surface and wipe lightly.

- Isopropyl alcohol, ethyl alcohol, trichlorotriflorothane

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface. Do not use the following solvent;

-water, ketone, aromatics

#### (2) CAUTION AGAINST STATIC CHARGE

The LCD modules use CMOS LSI drivers, so customers are recommend that any unused input terminal would be connected to  $V_{DD}$  or  $V_{SS}$ , do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

Remove the protective film slowly and, if possible, under ESD control device like ion blower and humidity of working room should be kept over 50%RH to reduce risk of static charge.

#### (3) PACKAGING

Avoid intense shock and falls from a height and do not operate or store them exposed direct to sunshine or high temperature/humidity.

#### (4) CAUTION FOR OPERATION

It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage than the limit causes the shorter LCD life. The use of direct current drive should be avoided because an electrochemical reaction due to direct current causes LCD's undesirable deterioration.

Response time will be extremely delayed at low temperature, and LCD's show dark color at high temperature. However those phenomena do not mean malfunction or out of order with LCD's.

Some font will be abnormally displayed when the display area is pushed hard during operation. But it resumes normal condition after turning off once.

#### (5) SOLDERING (for Pin type)

It is recommended to complete dip soldering at  $270\,^{\circ}\text{C}$  or hand soldering at  $280\,^{\circ}\text{C}$  within 3 seconds. The soldering position is at least 3mm apart from the pin head. Wave or reflow soldering are not recommended. Metal pins should not be soldered for more than 3 times and each soldering should be done after cool down of metal pins

#### (6) SAFETY

For crash damaged or unnecessary LCD's, it is recommended to wash off liquid crystal by either of solvents such as acetone and ethanol and should be burned up later.

When any liquid leaked out of a damaged glass cell comes in contact with your hands, wash it off with soap and water.

#### WARRANTY

CLOVER will replace or repair any of her LCD module in accordance with her LCD specification for a period of one year from date of shipment. The warranty liability of Clover is limited to repair and/or replacement. Clover will not be responsible for any subsequent or consequential event.

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### **APPENDIX**

# LOT INDICATION OF LCD MODULE

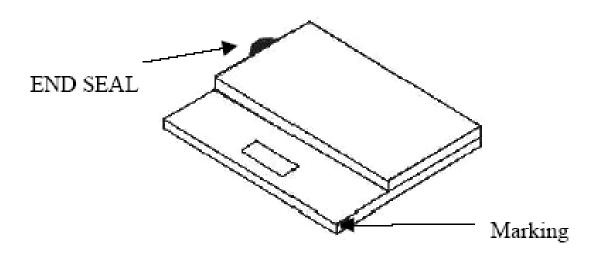
### CODING SYSTEM:



# COLOR CODE:

MONTH	COLOR		
1	BROWN	棕	
2	RED	紅	
3	ORANGE	橙	
4	YELLOW	黄	
5	GREEN	綠	
6	BLUE	藍	
7	PURPLE	紫	
8	GREY	灰	
9	WHITE	白	
10	BLACK	黑	
11	GOLD	金	
12	SILVER	銀	

# 3 TYPES OF LOCATION AS SHOWN BELOW:



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