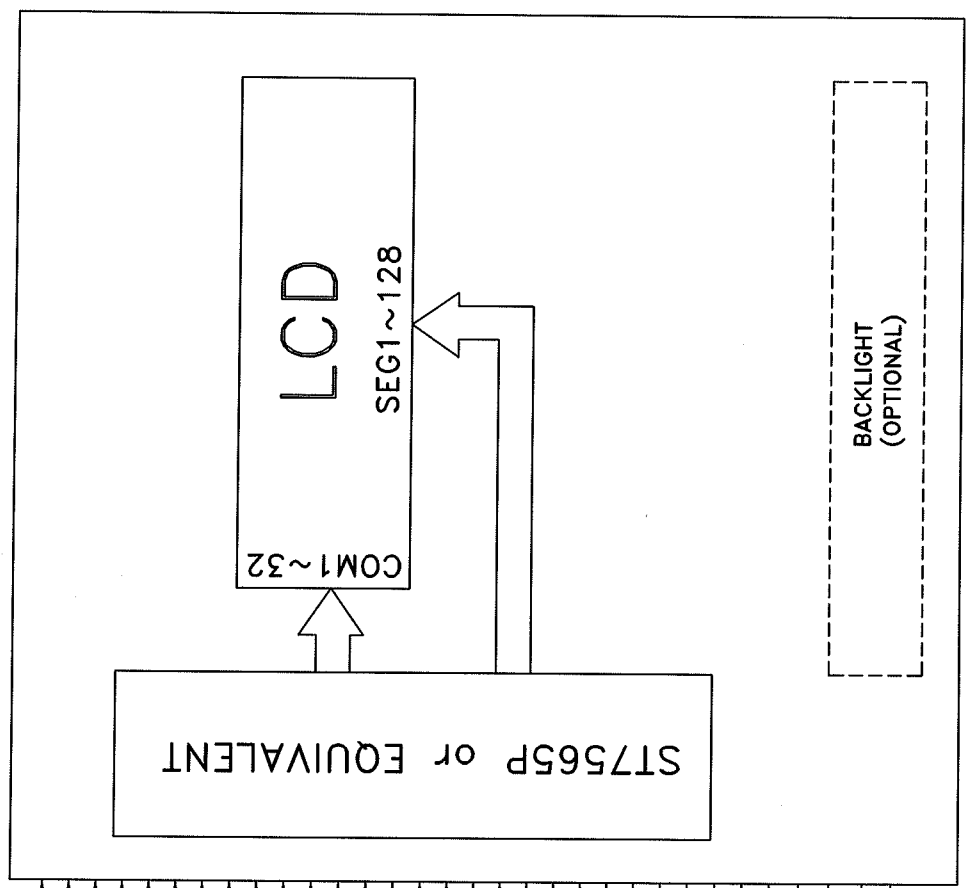


- (1) P/S
- (2) C86
- (3) V5
- (4) V4
- (5) V3
- (6) V2
- (7) V1
- (8) CAP4+
- (9) CAP2+
- (10) CAP2-
- (11) CAP1-
- (12) CAP1+
- (13) CAP3-
- (14) CAP5-
- (15) VOUT
- (16) VSS
- (17) VDD
- (18) D7
- (19) D6
- (20) D5
- (21) D4
- (22) D3
- (23) D2
- (24) D1
- (25) D0
- (26) /RD(E)
- (27) /WR(R/W)
- (28) A0
- (29) /RES
- (30) /CS
- *(31) A
- *(32) K



| PIN NUMBER | SYMBOL | FUNCTION |
|------------|----------|--------------------------------------|
| 1 | P/S | Parallel/Serial select |
| 2 | C86 | 8080 / 6800 MPU interface select |
| 3 | V5 | |
| 4 | V4 | |
| 5 | V3 | |
| 6 | V2 | Power supply for LCD drive |
| 7 | V1 | |
| 8 | CAP4+ | |
| 9 | CAP2+ | |
| 10 | CAP2- | |
| 11 | CAP1- | |
| 12 | CAP1+ | |
| 13 | CAP3- | |
| 14 | CAP5- | |
| 15 | VOUT | |
| 16 | VSS | DC/DC converter for VLCD terminal |
| 17 | VDD | |
| 18 | D7 | Logic power supply(OV) |
| | | Logic power supply |
| | | Parallel mode: date bus |
| | | Serial mode: serial data input (SI) |
| 19 | D6 | Parallel mode: date bus |
| | | Serial mode: serial data input (SCL) |
| 20 | D5 | |
| 21 | D4 | |
| 22 | D3 | |
| 23 | D2 | |
| 24 | D1 | |
| 25 | D0 | Data bus |
| 26 | /RD(E) | Read enable for 8080 MPU |
| | | Read/Write clock for 6800 MPU |
| 27 | /WR(R/W) | Write enable for 8080 MPU |
| | | Read/Write control for 6800 MPU |
| 28 | A0 | Date/Instruction select |
| 29 | /RES | Reset terminal |
| 30 | /CS | Chip select terminal |
| 31 | A | Voltage supply for backlight (VE+) |
| 32 | K | Voltage supply for backlight (VE-) |

| | | | | | | |
|---------------------------------|---------------------------|--------------------|----------------|--------------------------------------|-------------------|--|
| TOLERANCE IF NOT SPECIFY ±0.5mm | | SCALE NOT ON SCALE | UNIT IN mm | CLOVER CHINA DISPLAY (SHENZHEN) LTD. | | SHEET 2 OF 3 |
| REV. 00 | REVISION RECORD 1st ISSUE | | DATE 10 FEB 12 | APPROVED | CUSTOMER STANDARD | MODEL NO. ZCG12832M |
| | | | | APPROVED | AGENT | TITLE: PINOUT & BLOCK DIAGRAM |
| | | | | OUR REF. ZX90901X | CUSTOMER REF. | DRAWN BY: LINSHAO DATE :10 FEB 12 |
| | | | | | | CHECKED BY : LinShao DATE :10 FEB 12 |
| | | | | | | APPROVED BY: <i>[Signature]</i> DATE : 10 FEB 12 |

1.General specification

Display mode : 128 x 32 Dots COG LCD module
 Interface : 8 bit parallel or serial
 Driving method : 1/33 duty , 1/9 bias

2.Electrical specification

Supply voltage for logic(VDD) : 3.3V
 Input voltage for LCD (Vlcd) : 8.0V (Generated by internal booster)

3.Backlight specification

Backlight type : Side-lited LED backlight
 Backlight color : White/Blue/Yellow green
 Backlight voltage : 3.3V @ 40mA

4.Mechanical specification

Viewing area[mm] : 47.0(L)x18.4(W)
 Module dimension[mm] : 59.8(L)x35.1(W)x2.8(H1)/6.5MAX.(H2)

| | | | | | | |
|------------------------------------|-----------------|-----------------------|-------------------|--------------------------------------|----------------------|-----------------|
| TOLERANCE IF NOT SPECIFY ±0.5mm | | SCALE NOT ON SCALE | UNIT IN mm | CLOVER CHINA DISPLAY (SHENZHEN) LTD. | | SHEET 3 OF 3 |
| REV. | REVISION RECORD | | | APPROVED | MODEL NO. ZCG12832M | |
| 00 | 1st ISSUE | | DATE 10 FEB 12 | APPROVED | TITLE: SPECIFICATION | |
| | | | | AGENT | DRAWN BY: LINSHAO | DATE :10 FEB 12 |
| | | | | | CHECKED BY : LinShao | DATE :10 FEB 12 |
| | | | | | OUR REF. ZX90901X | DATE 02/2/12 |
| | | | | | CUSTOMER REF. | |