	<b>CLOVER DISPLAY LTD.</b>					
LCD MODULE SPECIFICATION Model : CV9021A						
		<b>I</b>				
		Revision	05			
		Engineering	Timmy Kwan			
		Date	25 July 2008			
		Our Reference	9021			
ADDRESS : TEL : FAX : E-MAIL : URL :	1 <sup>st</sup> FLOOR, EFFICIENCY HOUSE, KOWLOON, HONG KONG. (852) 2341 3238 (SALES OFFICE) (852) 2357 4237 (SALES OFFICE) cdl@cloverdisplay.com http://www.cloverdisplay.com					

#### **MODE OF DISPLAY**

#### **Display mode Display condition Viewing direction** STN : Yellow green Reflective type 6 O' clock Grey Transflective type $\square$ 12 O' clock Blue (negative) Transmissive type 3 O' clock ☐ FSTN positive ☐ Others 9 O' clock FSTN negative

# LCD MODULE NUMBER NOTATION:

<u>CV9021</u>	<u>A- N N - S R - N 6 - T</u>
(1)	(2) (3) (4) (5) (6) (7) (8)

\*(1)---Model number of standard LCD Modules \*(2)---Backlight type N – No backlight E – EL backlight L – Side-lited LED backlight M-Array LED backlight C - CCFL\*(3)---Backlight color N – No backlight A – Amber B – Blue O– Orange W–White Y – Yellow green \*(4)---Display mode T - TNV – TN (Negative) S – STN Yellow green G – STN Grey B – STN Blue (Negative) F-FSTN N – FSTN (Negative) \*(5)---Rear polarizer type R – Reflective F – Transflective T – Transmissive \*(6)---Temperature range N – Normal W-Extended \*(7)---Viewing direction 6-6 O'clock 2 - 12 O'clock 3 - 3 O'clock 9 - 9 O'clock \*(8)---Special code for other requirements

(Can be omitted if not used)

# **GENERAL DESCRIPTION**

Display mode	:	100 x 65 dots, Graphic COG LCD module
Interface	:	Serial
Driving method	:	1/65 duty, 1/9 bias
Controller IC	:	SITRONIX ST7565P or equivalent For the detailed information, please refer to the IC specifications

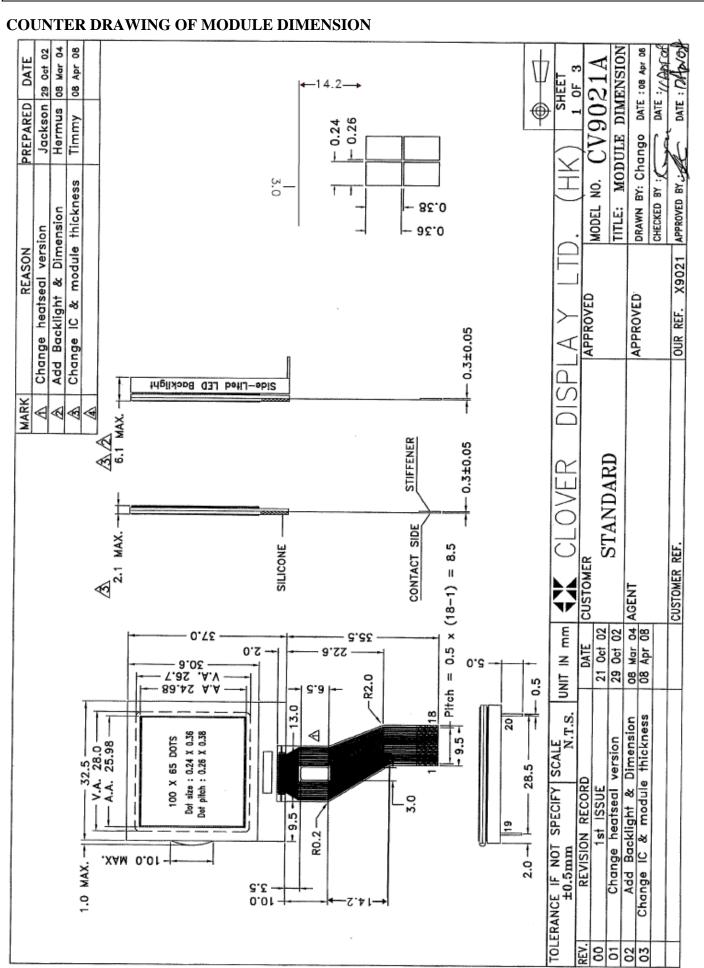
# MECHANICAL DIMENSIONS

Item	Dimension	Unit	Item	Dimension	Unit
Outline Dimension			Viewing Area	28.0(L)x26.7(W)	mm
No Backlight (N)	32.5(L)x37.0(W)x2.1(Max)(H)	mm	Dot Pitch	0.26(L)x0.38(W)	mm
LED Sided Backlight(L)	32.5(L)x37.0(W)x6.1(Max)(H)	mm	Dot Size	0.24(L)x0.36(W)	mm

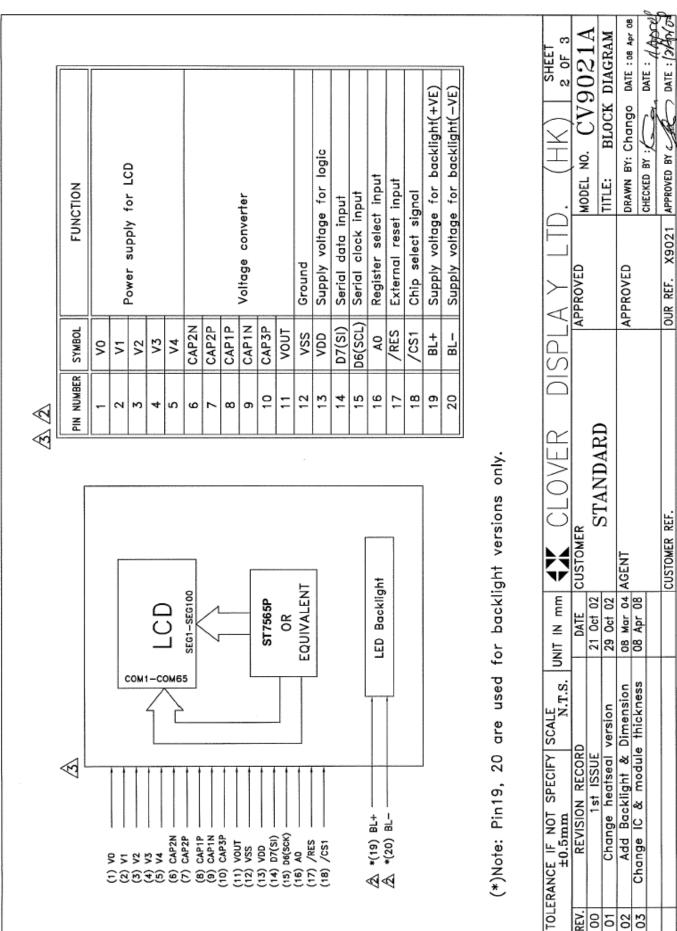
## CONNECTOR PIN ASSIGNMENT

Pin No.	Symbol	Function
1	V0	
2	V1	
3	V2	Power supply for LCD
4	V3	
5	V4	
6	CAP2N	
7	CAP2P	
8	CAP1P	Voltage Converter
9	CAP1N	
10	CAP3P	
11	VOUT	
12	VSS	Ground
13	VDD	Supply voltage for Logic
14	D7(SI)	Serial data input
15	D6(SCL)	Serial clock input
16	A0	Register select input
17	/RES	Reset
18	/CS1	Chip select signal
*19	BL+	Supply Voltage for backlight(+VE)
*20	BL-	Supply Voltage for backlight(-VE)

Note (\*): Pin 19, 20 are for side-lited LED backlight versions only



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# COUNTER DRAWING OF PIN OUT & BLOCK DIAGRAM

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#### **ELECTRICAL CHARACTERISTICS** Conditions: VSS=0V, @Ta=25°C Item Symbol MIN. TYP. MAX. Symbol Unit Item MIN. TYP. MAX. Unit Supply Voltage VDD 3.05 3.30 3.55 V "H"Level Input Voltage VIH 0.8 VDD \_ VDD V Idd 150 VSS Supply Current \_ \_ μΑ "L"Level Input Voltage VIL \_ 0.2 VDD V Operating voltage for VOUT 11.8 12.0 12.2 V \_ \_ \_ LCD (\*) EL Backlight Voltage (VEL) **Backlight Current** EL (@ Frequency 400Hz) \_ Side-lited LED Backlight Forward Voltage (VF) Side-lited LED Backlight Forward Current (IF) White VBL 3.2 3.5 V White IBL 40 50 mA

Note (\*): Please refer to Connection Example (4X Boosting Circuit)

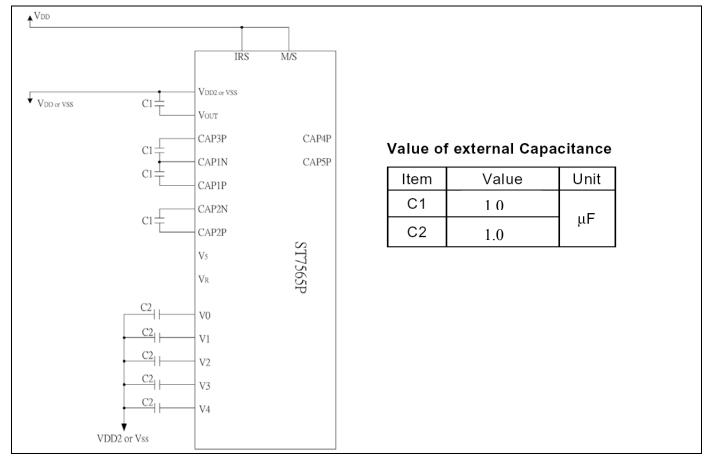
#### ABSOLUTE MAXIMUM RATINGS

Please make sure not to exceed the following maximum rating values under the worst application conditions

Item	Symbol	Rating (for normal temperature)	Rating (for wide temperature)	Unit
Supply Voltage	VDD	-0.3 to 3.6	-0.3 to 3.6	V
Input Voltage	Vin	-0.3 to VDD+0.3	-0.3 to VDD+0.3	V
Operating Temperature	Topr	0 to 50	-20 to 70	°C
Storage Temperature	Tstg	-10 to 60	-30 to 80	°C

## **CONNECTION EXAMPLE**

#### 4X Boosting Circuit



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# **INSTRUCTION TABLE**

Command		Command Code										
		/RD	/RD /WR	D7	D6	D5	D4 D	D3	D2	D1	D0	- Function
(1) Display ON/OFF	0	1	0	1	0	1	0	1	1	1	0 1	LCD display ON/OFF 0: OFF, 1: ON
(2) Display start line set	0	1	0	0	1	Di	spla	y sta	art a	ddr	ess	Sets the display RAM display sta line address
(3) Page address set	0	1	0	1	0	1	1		-		ress	Sets the display RAM page address
(4) Column address set upper bit Column address set lower bit	0 0	1 1	0 0	0 0	0 0	0 0	1 0	colu Lea	umn ist s	ado igni	icant dress ficant dress	Sets the most significant 4 bits of the display RAM column address Sets the least significant 4 bits of the display RAM column address
(5) Status read	0	0	1		St	atus		0	0	0	0	Reads the status data
(6) Display data write	1	1	0			١	Write	e dat	ta			Writes to the display RAM
(7) Display data read	1	0	1			F	Read	d dat	ta			Reads from the display RAM
(8) ADC select	0	1	0	1	0	1	0	0	0	0	0 1	Sets the display RAM address SEG output correspondence 0: normal, 1: reverse
(9) Display normal/ reverse	0	1	0	1	0	1	0	0	1	1	0 1	Sets the LCD display normal/ reverse 0: normal, 1: reverse
(10) Display all points ON/OFF	0	1	0	1	0	1	0	0	1	0	0 1	Display all points 0: normal display 1: all points ON
(11) LCD bias set	0	1	0	1	0	1	0	0	0	1	0 1	Sets the LCD drive voltage bias ratio 0: 1/9 bias, 1: 1/7 bias (ST7565)
(12) Read/modify/write	0	1	0	1	1	1	0	0	0	0	0	Column address increment At write: +1 At read: 0
(13) End	0	1	0	1	1	1	0	1	1	1	0	Clear read/modify/write
(14) Reset	0	1	0	1	1	1	0	0	0	1	0	Internal reset
(15) Common output mode select	0	1	0	1	1	0	0	0 1	*	*	*	Select COM output scan direction 0: normal direction 1: reverse direction
(16) Power control set	0	1	0	0	0	1	0	1		bera bde	ating	Select internal power supply operating mode
<li>(17) V₀ voltage regulator internal resistor ratio set</li>	0	1	0	0	0	1	0	0		esist atio	tor	Select internal resistor ratio(Rb/Ra) mode
(18) Electronic volume mode set Electronic volume register set	0	1	0	1 0	0 0	0 Ele	0 ctroi	0 nic v	0 olur	0 ne v	1 value	Set the Vo output voltage electronic volume register
(19) Static indicator ON/OFF Static indicator	0	1	0	1	0	1	0	1	1		1	0: OFF, 1: ON
register set				0	0	0	0	0	0	0	Mode	Set the flashing mode
(20) Booster ratio set	0	1	0	1 0	1 0	1 0	1 0	1 0	0 0		0 ep-up alue	select booster ratio 00: 2x,3x,4x 01: 5x 11: 6x
(21) Power saver												Display OFF and display all points ON compound command
(22) NOP	0	1	0	1	1	1	0	0	0	1	1	Command for non-operation
(23) Test	0	1	0	1	1	1	1	*	*	*	*	Command for IC test. Do not use this command

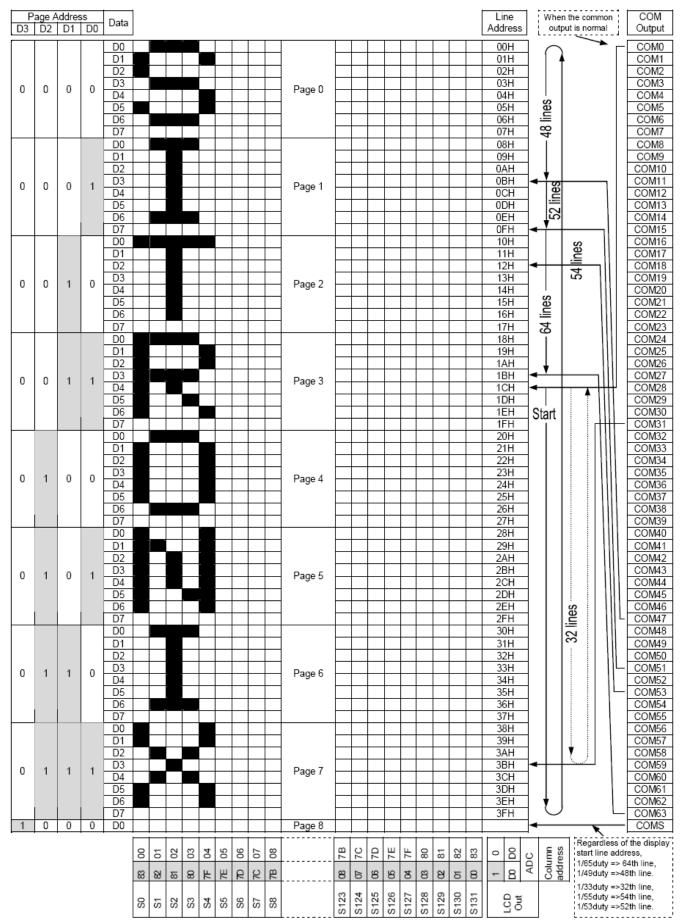
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#### **RECOMMENDED INITIAL SETTINGS**

Initial Display Line : 40H LCD Bias Select : A2H Power Control : 2FH Regulator Resistor Select : 26H Set Reference Voltage Register : 36H SHL Select : C8H

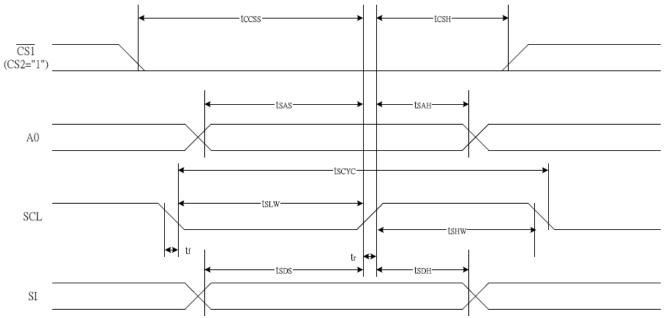
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#### **DISPLAY DATA RAM (DDRAM)**



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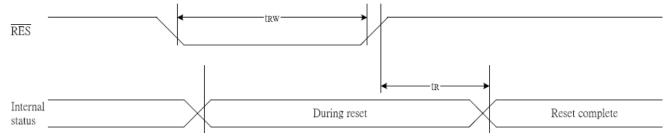
## SERIAL INTERFACE TIMING DIAGRAM



#### SERIAL INTERFACE TIMING CHARACTERISTICS

				(VDD = 3.3V,	Ta = -30 to	o 85°C)
ltem	Signal	Symbol	Condition	Rat	Rating	
item	Signal	Symbol	Condition	Min.	Max.	Units
Serial Clock Period		Tscyc		50	_	
SCL "H" pulse width	SCL	Tshw		25	_	]
SCL "L" pulse width		Tslw		25	_	]
Address setup time	4.0	TSAS		20	_	7
Address hold time	A0	Tsah		10	_	ns
Data setup time	SI	Tsds		20	_	1
Data hold time	51	TSDH		10	_	7
CS-SCL time	00	Tcss		20	_	1
CS-SCL time	CS	Tcsh		40	_	1

## **RESET TIMING DIAGRAM**



## **RESET TIMING**

				(Vi	DD = 3.3V,	Ta = -30 to	o 85°C)
Item	Signal	Symbol	Condition		Units		
item	Signal	Symbol	Condition	Min.	Тур.	Max.	Units
Reset time		tR			—	1.0	us
Reset "L" pulse width	/RES	trw		1.0	_	_	us

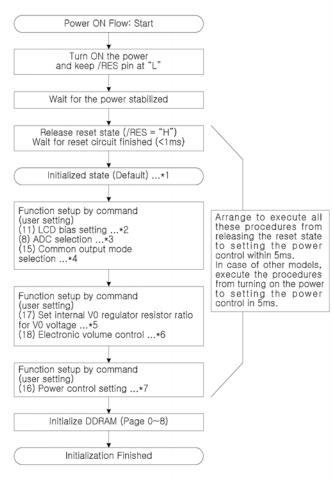
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## THE RESET CIRCUIT

When the /RES input comes to the "L" level, these LSIs return to the default state. Their default states are as follows:

- 1. Display OFF
- 2. Normal display
- 3. ADC select: Normal (ADC command D0 = "L")
- 4. Power control register: (D2, D1, D0) = (0, 0, 0)
- 5. Serial interface internal register data clear
- LCD power supply bias rate: 1/65 DUTY = 1/9 bias 1/49,1/55,1/53 DUTY = 1/8 bias 1/33 DUTY = 1/6 bias
- All-indicator lamps-on OFF (All-indicator lamps ON/OFF command D0 = "L")
- 8. Power saving clear
- 9. Vo voltage regulator internal resistors Ra and Rb separation
- 10. Output conditions of SEG and COM terminals SEG=VSS, COM=VSS
- 11. Read modify write OFF
- Static indicator OFF Static indicator register : (D1, D2) = (0, 0)
- 13. Display start line set to first line
- 14. Column address set to Address 0
- 15. Page address set to Page 0
- 16. Common output status normal
- 17. Vo voltage regulator internal resistor ratio set mode clear
- 18. Electronic volume register set mode clear Electronic volume register :
- (D5, D4, D3, D2, D1, D0) = (1, 0, 0, 0, 0)
- 19. Test mode clear

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\* The target time of 5ms will result to vary depending on the panel characteristics and the capacitance of the smoothing capacitor. Therefore, we suggest you to conduct an operation check using the actual equipment.

Notes: Refer to respective sections or paragraphs listed below.

- \*1: Description of functions; Resetting circuit
- \*2: Command description; LCD bias setting
- \*3: Command description; ADC selection
- \*4: Command description; Common output state selection
- \*5: Description of functions; Power circuit & Command description; Setting the built-in resistance radio for regulation of the V0 voltage
- \*6: Description of functions; Power circuit & Command description; Electronic volume control
- \*7: Description of functions; Power circuit & Command description; Power control setting

# **ELECTRO-OPTICAL CHARACTERISTICS**

MEASURING CONDITION:

POWER SUPPLY =  $V_{OP}$  / 64 Hz TEMPERATURE = 23 ± 5 °C RELATIVE HUMIDITY = 60 ± 20 %

	KEEMIN E HOWIDH I 00 ±	20 /0	
ITEM	SYMBOL	UNIT	TYP. STN
RESPONSE TIME	Ton	ms	220
	Toff	ms	280
CONTRAST RATIO	Cr	-	12
	V3:00	0	40
VIEWING ANGLE	V6:00	0	70
(6 O'clock)	V9:00	0	40
$Cr \ge 2$	V12:00	0	50

THE ELECTRO-OPTICAL CHARACTERISTICS ARE MEASURED VALUE BUT NOT GUARANTEED ONES.

## **RELIABILITY OF LCD MODULE**

	TEST CONDITION	TEST CONDITION	
ITEM	FOR NORMAL TEMPERATURE	FOR WIDE TEMPERATURE	TIME
High temperature operating	50°C	70°C	240 hours
Low temperature operating	0°C	-20°C	240 hours
High temperature storage	60°C	80°C	240 hours
Low temperature storage	-10°C	-30°C	240 hours
Temperature-humidity storage	40°C 90% R.H.	60°C 90% R.H.	96 hours
Temperature cycling	-10°C to 60°C	-30°C to 80°C	5 avala
	30 Min Dwell	30 Min Dwell	5 cycle
Vibration Test at LCM Level	Freq 10-55 Hz	Freq 10-55 Hz	
	Sweep rate: 10-55-10 at 1 min	Sweep rate: 10-55-10 at 1 min	
	Sweep mode Linear	Sweep mode Linear	—
	Displacement: 2 mm p-p	Displacement: 2 mm p-p	
	1 Hour each for X, Y, Z	1 Hour each for X, Y, Z	

# SAMPLING METHOD

SAMPLING PLAN:	MIL-STD 105E			
CLASS OF AQL:	LEVEL II/ SINGLE SAMPLING			
	MAJOR-0.65%	MINOR – 1.5%		

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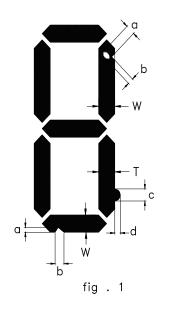
# QUALITY STANDARD

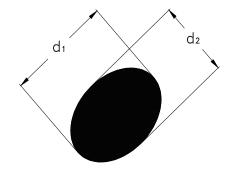
DEFECT	CRITERIA		ТҮРЕ	FIGURE
SHORT CIRCUIT	_		MAJOR	-
MISSING SEGMENT	-		MAJOR	-
UNEVEN / POOR CONTRAST	-		MAJOR	-
CROSS TALK	-		MAJOR	-
PIN HOLE	$MAX(a,b) \leq 1/4 W$		MINOR	1
EXCESS SEGMENT	$MAX(c,d) \leq 1/4 T$		MINOR	1
BUBBLES	$d^* \ge 0.2$	QTY=0	MINOR	2
BLACKS SPOTS	$d \leq 0.3$	N.A.**	MINOR	2
	0.3 <d≤0.4< td=""><td>QTY≤1</td><td></td><td></td></d≤0.4<>	QTY≤1		
	0.4 <d< td=""><td>QTY=0</td><td></td><td></td></d<>	QTY=0		
LINE SCRATCHES	x≥0.7 y≥0.05	QTY=0	MINOR	3
BLACK LINE	x≥0.7 y≥0.05	QTY=0	MINOR	3

 $d = MAX(d_1,d_2)$ 

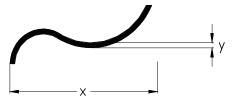
\*\* N. A . = NOT APPLICABLE

DEFECT TABLE : B





POLARIZER BUBBLES / SPOTS fig . 2



LINE SCRATCHES / BLACK LINE fig . 3  $\,$ 

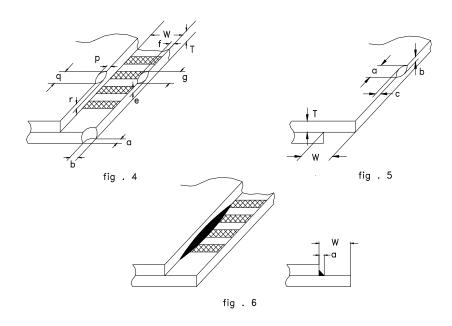
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# QUALITY STANDARD ( CONT .)

DEFECT		CRITERIA	ТҮРЕ	FIGURE
	CONTACT EDGE	e≤1/2T f≤1/3W g≤3.5		4
CHIPS	BOTTOM GLASS	p≤1.0 q≤3.5 r≤1/2T	MINOR	4
	CORNER	a≤1.5 b≤W		4
	TOP GLASS	a≤3.0 b≤1/3T c≤1/2W		5
GLASS PF	ROTRUSION	$a \le 1/4 \ W$	MINOR	6
RAINBOW	V	_	MINOR	-

UNLESS STATE OTHERWISE , ALL UNIT ARE IN MILLIMETER .

DEFECT TABLE : B



#### HANDLING PRECAUTIONS

#### (1) CAUTION OF LCD HANDLING & CLEANING

Use soft cloth with solvent (recommended below) to clean the display surface and wipe lightly. - Isopropyl alcohol, ethyl alcohol, trichlorotriflorothane

Do not wipe the display surface with dry or hard materials that will damage the polarizer surface. Do not use the following solvent; -water, ketone, aromatics

#### (2) CAUTION AGAINST STATIC CHARGE

The LCD modules use CMOS LSI drivers, so customers are recommend that any unused input terminal would be connected to  $V_{DD}$  or  $V_{SS}$ , do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

Remove the protective film slowly and, if possible, under ESD control device like ion blower and humidity of working room should be kept over 50%RH to reduce risk of static charge.

#### (3) PACKAGING

Avoid intense shock and falls from a height and do not operate or store them exposed direct to sunshine or high temperature/humidity.

#### (4) CAUTION FOR OPERATION

It is an indispensable condition to drive LCD's within the specified voltage limit since the higher voltage than the limit causes the shorter LCD life. The use of direct current drive should be avoided because an electrochemical reaction due to direct current causes LCD's undesirable deterioration.

Response time will be extremely delayed at low temperature, and LCD's show dark color at high temperature. However those phenomena do not mean malfunction or out of order with LCD's.

Some font will be abnormally displayed when the display area is pushed hard during operation. But it resumes normal condition after turning off once.

#### (5) SOLDERING (for Pin type)

It is recommended to complete dip soldering at 270 °C or hand soldering at 280 °C within 3 seconds. The soldering position is at least 3mm apart from the pin head. Wave or reflow soldering are not recommended. Metal pins should not be soldered for more than 3 times and each soldering should be done after cool down of metal pins

#### (6) SAFETY

For crash damaged or unnecessary LCD's, it is recommended to wash off liquid crystal by either of solvents such as acetone and ethanol and should be burned up later.

When any liquid leaked out of a damaged glass cell comes in contact with your hands, wash it off with soap and water.

## WARRANTY

CLOVER will replace or repair any of her LCD module in accordance with her LCD specification for a period of one year from date of shipment. The warranty liability of Clover is limited to repair and/or replacement. Clover will not be responsible for any subsequent or consequential event.