

### LCD MODULE SPECIFICATION

Model: CV12032A - \_ \_ - \_ - \_ -

Revision	06
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Date	12 September 2006
Our Reference	4908

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### MODE OF DISPLAY

Display mode  STN: Yellow green Grey Blue (negative) FSTN positive FSTN negative	Refle	condition ective type sflective type smissive type rs	Viewing direction  ☐ 6 O' clock ☐ 12 O' clock ☐ 3 O' clock ☐ 9 O' clock
LCD MODULE NUMBER NOT	ATION:		
CV12032A- MY - S F - N 6		*(2)Backlight to N - E - L - M - C - *(3)Backlight to N - A - B - O - W - Y - *(4)Display mo T - V - S - G - B - F - N - *(5)Rear polari R - F - T - *(6)Temperatur N - W - *(7)Viewing di 6 - 2 - 3 - 9 - *(8)Special cool (Can be om T - T - T - T - T - T - T - T - T - T	- No backlight - EL backlight - Side-lited LED backlight - Array LED backlight - CCFL - Color - No backlight - Amber - Blue - Orange - White - Yellow green - Vellow green - TN - TN (Negative) - STN Yellow green - STN Grey - STN Blue (Negative) - FSTN - FSTN (Negative) zer type - Reflective - Transflective - Transmissive - re range - Normal - Extended

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#### **GENERAL DESCRIPTION**

Display mode : 120 X 32 dots, Graphic LCD module

Interface : 8-bit parallel

Driving method : 1/32 duty, 1/5 bias

Controller IC : Epson SED1520 or equivalent

For the detailed information, please refer to the IC specifications.

#### **MECHANICAL DIMENSIONS**

Item	Dimension		Unit	Item	Dimension		Unit
Outline Dimension	84.0(L)x44	.0(W)x (H1/H2)	mm	Dot Pitch	0.47(L)x0.5	0.47(L)x0.58(W)	
Viewing Area	62.5(L)x23.5(W)		mm	Dot Size	0.39(L)x0.5	0.39(L)x0.50(W)	
No Backlight (N)	H1	7.9	mm	Side Backlight (L)	H1	10.4	mm
	H2	12.0	mm		H2	14.5	mm
EL Backlight (E)	H1	7.9	mm	Array Backlight (M)	H1	10.4	mm
	H2	12.0	mm		H2	14.5	mm

#### **CONNECTOR PIN ASSIGNMENT**

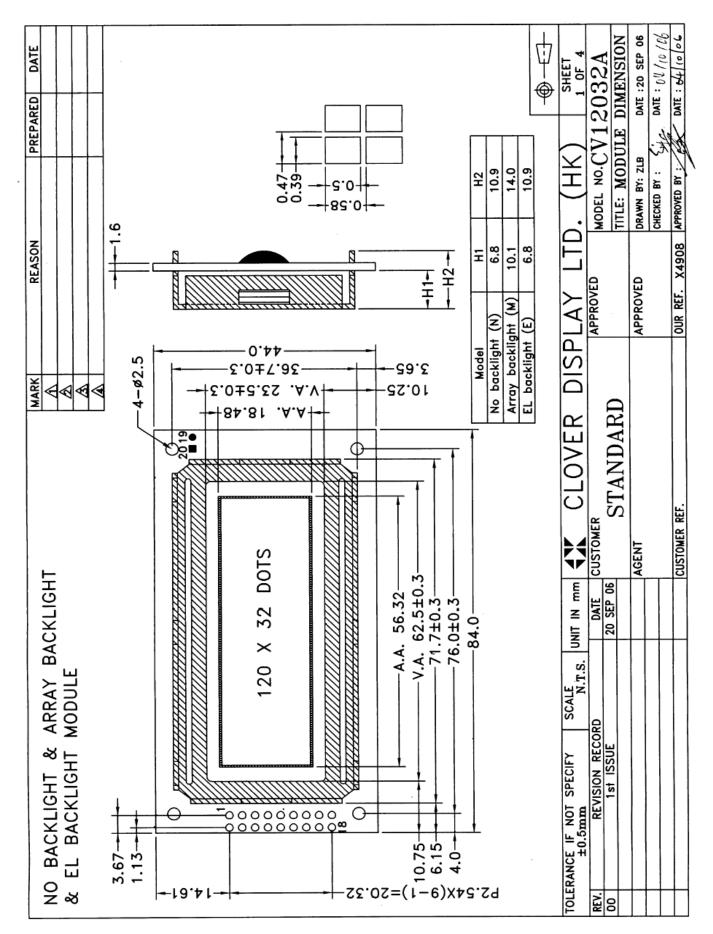
Pin No.	Symbol	Function	Pin No.	Symbol	Function
1	Vss	0V Power Supply	11	DB1	Data Bus Line
2	Vdd	5.0V Power Supply	12	DB0	Data Bus Line
3	Vo	LCD Drive, 0V to VDD (5.0V)	13	RW2	Read/Write **
4	RST	Reset Signal	14	RW1	Read/Write *
5	DB7	Data Bus Line	15	E2	Enable Signal **
6	DB6	Data Bus Line	16	E1	Enable Signal *
7	DB5	Data Bus Line	17	A02	Register Select Input **
8	DB4	Data Bus Line	18	A01	Register Select Input *
9	DB3	Data Bus Line	19	BL-	Backlight Terminal (-VE)
10	DB2	Data Bus Line	20	BL+	Backlight Terminal (+VE)

<sup>\*</sup> RW1, E1, A01 are used to control the left part of the display screen.

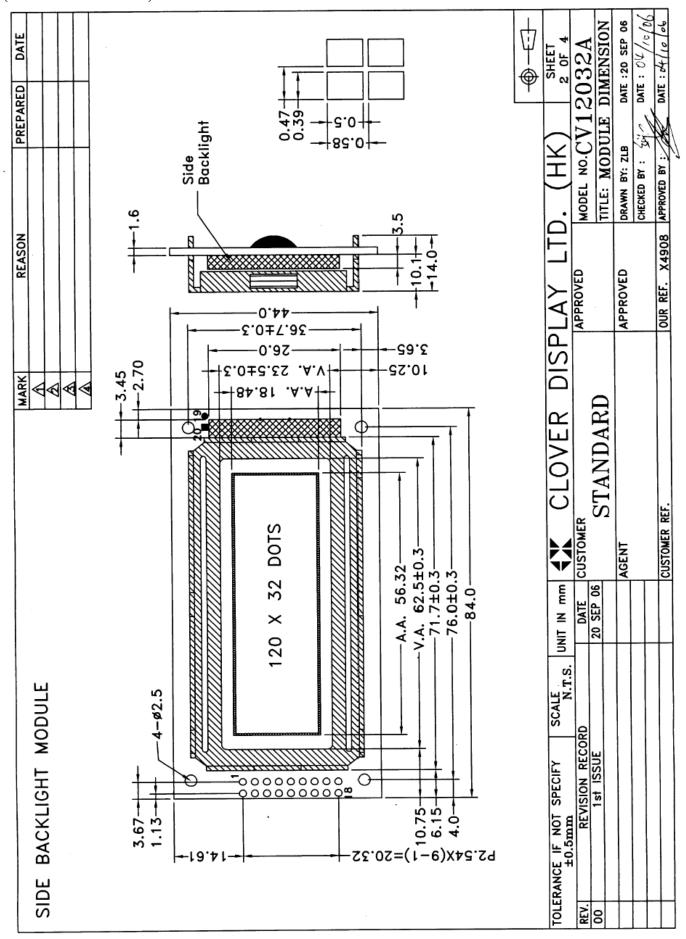
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<sup>\*\*</sup> RW2, E2, A02 are used to control the right part of the display screen.

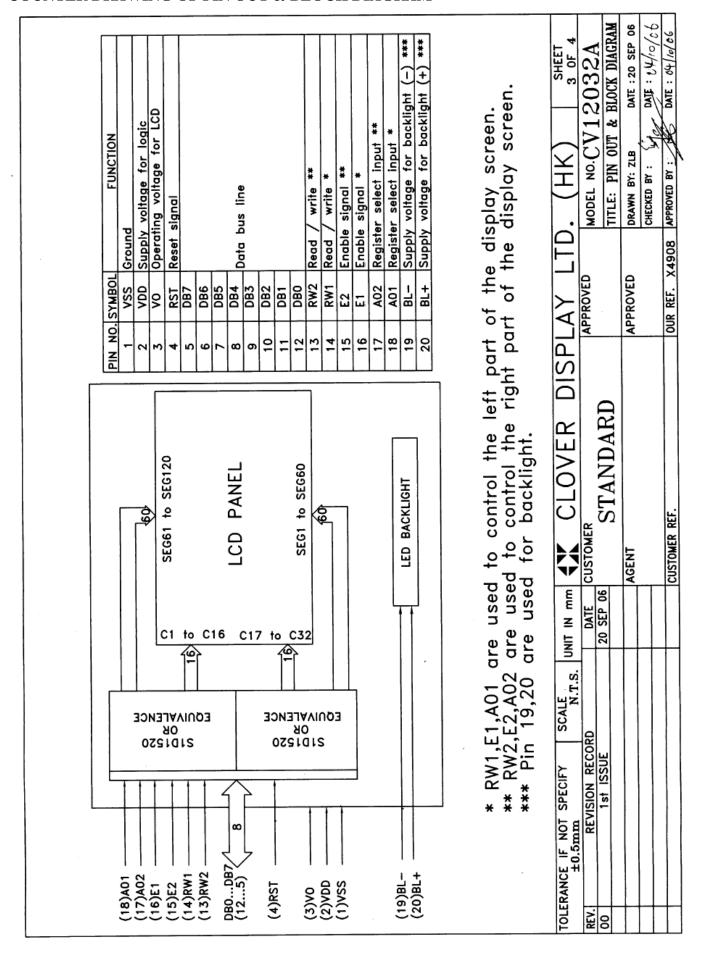
# COUNTER DRAWING OF MODULE DIMENSION (ARRAY & EL BACKLIGHT & NO BACKLIGHT)



# COUNTER DRAWING OF MODULE DIMENSION (SIDE BACKLIGHT)



#### COUNTER DRAWING OF PIN OUT & BLOCK DIAGRAM



#### **CLOVER DISPLAY LTD.** CV12032A **ELECTRICAL CHARACTERISTICS** Conditions: VSS=0V, @Ta=25°C TYP. MIN. MAX. Item Symbol Unit Symbol MIN. TYP. MAX. Item Unit Supply Voltage VDD 4.75 5.00 5.25 V 'H"Level Input Voltage VIH 2.2 VDD V Supply Current "L"Level Input Voltage 0 0.6 Idd 2.70 3.30 mA $V_{IL}$ \_ **Backlight Current** Backlight Voltage VEL 100 150 EL (@ Frequency 400Hz) Vrms Side-lited LED Side-lited LED White VBL 5.0 V White **I**BL 30 35 40 mA Blue $V_{BL}$ V Blue **I**BL mΑ Yellow Green V Yellow Green VBL **I**BL mA **Array LED** Array LED 4.25 V Yellow Green VBL 3.85 4.05 Yellow Green IBL 100 180 mΑ Amber $V_{BL}$ V Amber **I**BL mΑ Orange $V_{BL}$ V Orange **IBL** mΑ Soft Orange VBL V Soft Orange **I**BL mA

#### **ABSOLUTE MAXIMUM RATINGS**

VBL

**CCFL** 

White

Please make sure not to exceed the following maximum rating values under the worst application conditions

Item	Symbol	Rating (for normal temperature)	Rating (for wide temperature)	Unit
Supply Voltage	Vdd	7	7	V
Input Voltage	VT	-0.3 to VDD +0.3	-0.3 to VDD +0.3	V
Operating Temperature	Topr	0 to 50	-20 to 70	$^{\circ}\mathbb{C}$
Storage Temperature	Tstg	-10 to 60	-30 to 80	$^{\circ}\! \mathbb{C}$

CCFL

**I**BL

mArms

Vrms White

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#### INSTRUCTIONS

		Code						_		_	
Instruction	A0	R/W	DB7	DB6	DB5	DB4	DB3	DB2	DB1	DB0	Description
Display On/Off	0	0	1	0	1	0	1	1	1	0/1	Whole display On/Off. 1: On, 0: Off
Display Start Line	0	0	1	1	0		A	splay St Address (0-31)		ı	Determine the display line correspond to the COM0
Page Address Set	0	0	1	0	1	1	1	0		age 0-3)	Set the page of display data Ram to the page register.
Column Address Set	0	0	0			Col	umn A (0-59	ddress			Set the column address of display data RAM to the column register.
Status Read	0	1	Busy	ADC	ON/ OFF	RESE	T 0	0	0	0	Read the status.  BUSY 1: Working 0: Ready  ADC 1: Clockwise output 0: Counterclockwise  ON/OFF 1: Display Off 0: Display On  RESET 1: Reset 0: Normal
Write Display Data	1	0		•		Write 1	Data	•		•	Write the data to the display data RAM *
Read Display Data	1	1		_		Read I	Data	_		_	Read the data from the display data RAM *
ADC Select	0	0	1	0	1	0	0	0	0	0/1	Determine the clockwise or counterclockwise reading of the display data RAM.  0: Clockwise output  1: Counterclockwise output
Static Drive On/Off	0	0	1	0	1	0	0	1	0	0/1	Select the dynamic or static driving.  1: Static driving  0: Dynamic driving
Duty Ratio Select	0	0	1	0	1	0	1	0	0	0/1	Select the duty ratio. 1: 1/32 duty 0: 1/16 duty
Read Modify Write	0	0	1	1	1	0	0	0	0	0	Increment the column address register when writing but no-change when reading
End	0	0	1	1	1	0	1	1	1	0	Release from the read modify write mode.
Reset	0	0	1	1	1	0	0	0	1	0	Set the display start line register to 1st line, column add. counter and page add. register to "0"

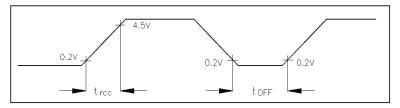
<sup>\*</sup>note: Access the predetermined address of the display data RAM. The column address increment "1" after read or write.

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#### TIMING CHARACTERISTICS OF COMPATIBLE CONTROLLER CHIPS

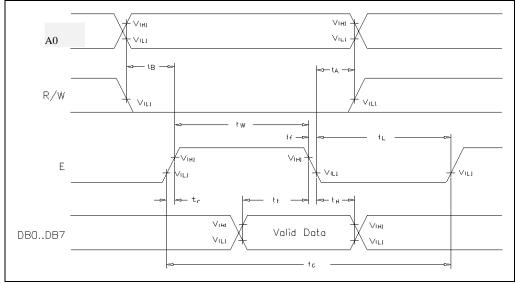
Parameters	Symbol	<b>Recommended timing</b>	Parameters	Symbol	Recommended timing
Enable Cycle Time	tC (min)	1000ns	Set-up Time	tB(min)	20ns
Enable Pulse Width			Data Set-up Time	tl (min)	80ns
High level	tW(min)	100ns	Data Delay Time	t <sub>D</sub> (max)	90ns
Low level	tL (min)	100ns	Address Hold Time	tA(min)	10ns
Enable Raise Time	tr (max)	15ns	Input Data Hold Time	tH (min)	10ns
Enable Fall Time	tf (max)	15ns	Output Data Hold Time	tD (min)	10ns

Figure 1 Power On Timing Diagram

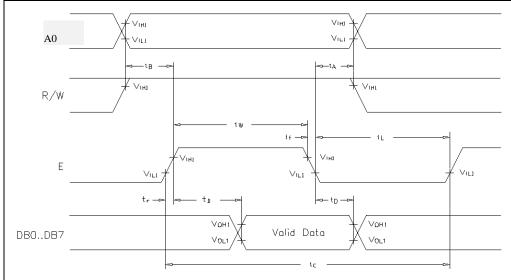


**Note:** Power on initialization depends on the rise time of the power supply when it is turned on. When the above power supply conditions is not met, the internal reset circuit will not operate normally and initialization will not be performed. Initialization by manual instruction is required.









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#### INITIALIZATION METHOD

The module will automatically perform initialization by detecting the rising or falling edge of the RST input after the power is turned on. The following instructions are executed during initialization.

- 1. Display Off
- 2. Set the 1st line to the display start register
- 3. Static drive Off
- 4. Set the address "0" to the column address counter
- 5. Set the page "3" to the page address register
- 6. Select the 1/32 duty
- 7. Select the ADC : Clockwise output
- 8. Read modify write mode Off

#### **DISPLAY DD RAM POSITION**

PERIOD ELLIP PERIOD	TINI I ODITION								
Page Address D0,D1	Data								
	DB0								
0,0	to				Page 0				
	DB7								
	DB0								
0,1	to				Page 1				
	DB7								
	DB0								
1,0	to				Page 2				
	DB7								
	DB0								
1,1	to				Page 3				
	DB7							-	
Column	ADC DB0=0	00	01	02		39	3A	3B	
Address	ADC DB0=1	3B	3A	39		02	01	00	

#### **ELECTRO-OPTICAL CHARACTERISTICS**

MEASURING CONDITION: POWER SUPPLY = Vop / 64 Hz

TEMPERATURE =  $22 \pm 5$  °C

RELATIVE HUMIDITY =  $60 \pm 15 \%$ 

ITEM	SYMBOL	UNIT	TYP. TN	TYP. STN
RESPONSE TIME	Ton	ms	100	200
	Toff	ms	80	200
CONTRAST RATIO	Cr	-	10	10
	V3:00	0	20	20
VIEWING ANGLE (6 O'clock)	V <sub>6:00</sub>	0	20	40
(Cr ≥ 2)	V9:00	0	20	20
	V12:00	0	10	10

THE ELECTRO-OPTICAL CHARACTERISTICS ARE MEASURED VALUE BUT NOT GUARANTEED ONES.

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#### RELIABILITY OF LCD MODULE

	TEST CONDITION	TEST CONDITION	
ITEM	FOR NORMAL TEMPERATURE	FOR WIDE TEMPERATURE	TIME
High temperature operating	50°C	70°C	240 hours
Low temperature operating	0°C	-20°C	240 hours
High temperature storage	60°C	80°C	240 hours
Low temperature storage	-10°C	-30°C	240 hours
Temperature-humidity storage	40°C 90% R.H.	60°C 90% R.H.	96 hours
Temperature cycling	-10°C to 60°C	-30°C to 80°C	5 cycles
	30 Min Dwell	30 Min Dwell	

### QUALITY STANDARD OF LCD MODULE

1.0	Sampling Method											
	Sampling Plan: MIL	STD 105 E										
	Class of AQL: Level II/Single Sampling Critical: 0.25% Major 0.65% Minor 1.5%											
	Critical: 0.25% Major 0.65% Minor 1.5%											
2.0	<b>Defect Group</b>	Failure Category	Failure Reasons									
	Critical Defect	Malfunction	Open									
	0.25%(AQL)		Short									
			Burnt or dead component									
			Missing part/improper part P.C.B.									
			Broken									
	Major Defect	Poor Insulation	Potential short									
	0.65%(AQL)		High current									
			Component damage or scratched									
			or Lying too close improper coating									
		Poor Conduction	Damage joint									
			Wrong polarity									
			Wrong spec. part									
			Uneven/intermittent contact									
			Loose part									
			Copper peeling									
			Rust or corrosion or dirt's									
	Minor Defect	Cosmetic Defect	Minor scratch									
	1.5%(AQL)		Flux residue									
			Thin solder									
			Poor plating									
			Poor marking									
			Crack solder									
			Poor bending									
			Poor packing									
			Wrong size									

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#### HANDLING PRECAUTIONS

#### (1) CAUTION OF LCD HANDLING & CLEANING

The polarizing plate on the surface of the panel is made from organic substances. Be very careful for chemicals not to touch the plate or it leads the polarizing plate to deteriorate.

If the use of a chemical is unavoidable, wipe the panel lightly with soft materials, such as gauze and absorbent cotton, soaked in a solvent.

\*Usable solvent: Alcohol (ethanol, IPA and the like)

Avoid wiping with a dry cloth, since it could damage the surface of the polarizing plate and others.

#### (2) CAUTION AGAINST STATIC CHARGE

The LCD modules use CMOS LSI drivers, so customers are recommended that any unused input terminal would be connected to  $V_{DD}$  or  $V_{SS}$ , do not input any signals before power is turned on, and ground your body, work/assembly areas, assembly equipment to protect against static electricity.

#### (3) PACKAGING

Avoid intense shock and falls from a height and do not operate or store them exposed to direct sunshine or high temperature/humidity for long periods.

#### (4) CAUTION FOR OPERATION

The viewing angle can be adjusted by varying the LCD driving voltage VO.

Driving voltage should be kept within specified range, excess voltage shortens display life.

Response time increases with decrease in temperature.

Display may turn black or dark Blue at temperature above its operational range; this is however not destructive and the display will return to normal once the temperature falls back to range.

Mechanical disturbance during operation (such as pressing on the viewing area) may cause the segments to appear "fractured". They will recover once the display is turned off.

Condensation at terminals will cause malfunction and possible electrochemical reaction. Relative humidity of the environment should therefore be kept below 60%.

#### (5) SAFETY

Liquid crystal may leak out of a damaged LCD, it is recommended to wash off the liquid crystal by using solvents such as acetone or ethanol and should be burned up later.

If any liquid leaks out of a damaged glass cell comes in contact with your hands, wash it off with soap and water immediately.

#### **WARRANTY**

CLOVER will replace or repair any of her LCD module in accordance with her LCD specification for a period of one year from date of shipment. The warranty liability of Clover is limited to repair and/or replacement. Clover will not be responsible for any subsequent or consequential event.

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<sup>\*</sup>Appropriate solvent: Ketones, ethyl alcohol